

WHAT IS CLAIMED IS:

1. An electronic circuit characterized by comprising:
 - a first transistor including a first terminal, a second terminal, and a first control terminal;
 - a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being connected to the first control terminal;
 - a capacitive element including a first electrode and a second electrode, the first electrode being connected to the first control terminal; and
 - a third transistor including a fifth terminal and a sixth terminal, the fifth terminal being connected to the second electrode, the second control terminal being connected to the third terminal.
2. An electronic circuit according to Claim 1,
 - further comprising a fourth transistor including a seventh terminal and an eighth terminal, the seventh terminal being connected to the fourth terminal.
3. An electronic circuit according to Claim 1,
 - the first terminal being connected to an electronic element.
4. An electronic circuit according to Claim 3,
 - the electronic element being a current-driven element.
5. An electronic circuit comprising:
 - a plurality of first signal lines;
 - a plurality of power lines; and
 - a plurality of unit circuits, each of the plurality of unit circuits comprising:
 - a first transistor including a first terminal, a second terminal, and a first control terminal;
 - a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being connected to the first control terminal;
 - a capacitive element including a first electrode and a second electrode, the first electrode being connected to the first control terminal; and
 - a third transistor including a fifth terminal, a sixth terminal, and a third control terminal, the fifth terminal being connected to the second electrode, the second control terminal being connected to the third terminal, and the third control terminal being connected to a respective first signal line of the plurality of first signal lines.
6. An electronic circuit according to Claim 5,

further comprising a plurality of second signal lines,
 each of the plurality of unit circuits further comprising a fourth transistor
 including a seventh terminal, an eighth terminal, and a fourth control terminal,
 the seventh terminal being connected to the fourth terminal, and
 the fourth control terminal being connected to a respective second signal line
 of the plurality of second signal lines.

7. An electronic circuit according to Claim 5,
 the first terminal being connected to an electronic element.
8. An electronic circuit according to Claim 7,
 the electronic element being a current-driven element.
9. An electronic circuit comprising:
 a holding element for holding a signal as a charge;
 a first switching transistor for controlling the transfer of the signal to the
 holding element;
 a driving transistor of which the conduction state is set according to the charge
 held by the holding element; and
 an adjustment transistor for setting a control terminal of the driving transistor
 to a predetermined potential prior to the transfer of the signal to the holding element.
10. An electronic circuit according to Claim 9,
 further comprising a second switching transistor for controlling the electronic
 connection or the electronic disconnection between the adjustment transistor and the
 predetermined potential.
11. An electronic circuit according to Claim 9,
 the driving transistor being connected to an electronic element.
12. An electronic circuit according to Claim 11,
 the electronic element being a current-driven element.
13. A driving method for an electronic circuit comprising:
 a first transistor including a first terminal, a second terminal, and a first control
 terminal;
 a second transistor including a third terminal and a fourth terminal, the third
 terminal being connected to the first control terminal; and
 a capacitive element including a first electrode and a second electrode, the first
 electrode being connected to the first control terminal,
 the driving method comprising:

a first step of electronically connecting the fourth terminal to a predetermined potential for setting the first control terminal to a first potential; and

a second step of, when the fourth terminal is electronically disconnected from the predetermined potential, changing the potential of the second electrode of the capacitive element from a second potential to a third potential to change the potential of the first control terminal from the first potential.

14. A driving method for the electronic circuit according to Claim 13, the potential of the second electrode being set to the second potential while at least the first step is being performed.

15. An electro-optical device comprising:
 a plurality of scanning lines;
 a plurality of data lines;
 a plurality of power lines; and
 a plurality of unit circuits having electro-optical elements,
 the electro-optical device characterized in that each of the plurality of unit circuits comprises:
 a first transistor including a first terminal, a second terminal, and a first control terminal;
 an electro-optical element connected to the first terminal;
 a second transistor including a third terminal, a fourth terminal, and a second control terminal, the third terminal being connected to the first control terminal;
 a capacitive element including a first electrode and a second electrode, the first electrode being connected to the first control terminal;
 a third transistor including a fifth terminal, a sixth terminal, and a third control terminal, the fifth terminal being connected to the second electrode; and
 a fourth transistor including a seventh terminal and an eighth terminal, the seventh terminal being connected to the fourth terminal,
 the second control terminal is connected to the third terminal,
 the third control terminal is connected to one of the plurality of scanning lines,
 and
 the sixth terminal is connected to one of the plurality of data lines.

16. An electro-optical device according to Claim 15, characterized in that the electro-optical element is an organic EL element.

17. An electro-optical device comprising a plurality of scanning lines, a plurality of data lines, a plurality of power lines, and a plurality of unit circuits having electro-optical elements,

the electro-optical device characterized in that each of the plurality of unit circuits comprises:

a first switching transistor of which the conduction state is controlled according to a scanning signal sent through one corresponding scanning line of the plurality of scanning lines;

a holding element for accumulating a data signal sent through one data line of the plurality of data lines and the first switching transistor, as a charge;

a driving transistor of which the continuity state is set according to the amount of the charge accumulated by the holding element, for supplying current having a current level according to the conduction state to the electro-optical elements; and

an adjustment transistor for setting a control terminal of the driving transistor to a predetermined potential prior to the transfer of the data signal to the holding element.

18. An electro-optical device according to Claim 17,

characterized in that each of the plurality of unit circuits comprises a second switching transistor for controlling the electronic connection or the electronic disconnection between the adjustment transistor and the predetermined potential.

19. An electro-optical device according to Claim 17,

characterized in that the electro-optical elements are organic EL elements.

20. A driving method for an electro-optical device comprising a plurality of scanning lines, a plurality of data lines, a plurality of power lines, and a plurality of unit circuits comprising:

a first transistor including a first terminal, a second terminal, and a first control terminal;

a second transistor including a third terminal and a fourth terminal, the third terminal being connected to the first control terminal; and

a capacitive element including a first electrode and a second electrode, the first electrode being connected to the first control terminal,

the driving method characterized by comprising:

a first step of electronically connecting the fourth terminal to a predetermined potential and of setting the first control terminal to a first potential; and

a second step of, after a scanning signal is sent through the plurality of scanning lines to a third transistor of which one end is connected to the second electrode to turn on the third transistor, and when the fourth terminal is electronically disconnected from the predetermined potential, applying the voltage corresponding to a data signal from the plurality of data lines to the second electrode through the third transistor to change the potential of the second electrode from a second potential to a third potential to change the potential of the first control terminal from the first potential.

21. A driving method for the electro-optical device according to Claim 20, characterized in that the potential of the second electrode of the capacitive element is set to the second potential while at least the first step is being performed.
22. An electronic apparatus characterized by having mounted thereon an electronic circuit according to Claim 1.
23. An electronic apparatus characterized by having mounted thereon an electro-optical device according to Claim 15.